



IFW

PATENT

UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: GILLILAND et al.

Application: STRUCTURES FOR IMPLEMENTING INTEGRATED CONDUCTOR  
AND CAPACITOR IN SMD PACKAGING

Serial No.: 10/760,448

Filing Date: January 20, 2004

Art Unit: 2831

Examiner: Eric W. Thomas

Case: ROC920030364US1

CERTIFICATE OF MAILING: I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed for Commissioner of Patents, P.O. Box 1450, Alexandria, VA, 22313-1450, on

Oct. 12, 2004 *Joan Pennington*  
Date of Deposit Signature

Joan Pennington

Typed/printed name of person signing

535 North Michigan Avenue  
Unit 1804  
Chicago, Illinois 60611

Mail Stop Amendment  
Honorable Commissioner Of Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

AMENDMENT

Sir:

In response to the Official Action mailed October 6, 2004, please amend  
the above-identified application as follows: